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		Application Number	10/605,628
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Filing Date	10/15/2003
		First Named Inventor	Jiang Liu
		Group Art Unit	2858
		Examiner Name	Unassigned
		Attorney Docket Number	1372.55.PRC
Sheet	1	of	1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
MCJ	1	VAN MOER ET AL.; Proving the Usefulness of a 3-Port Nonlinear Vectorial Network Analyser Through Mixer Measurements; IEEE MTT-S Digest; 2003.	
MCJ	2	TERROVITIS; Noise and Distortion Analysis of Active CMOS Mixers; Qualifying Examination Proposal.	
MCJ	3	FARIA ET AL.; The Use of Intermodulation Tables for Mixer Simulations; Microwave Journal; April 2002.	
MCJ	4	LIU ET AL.; A Table-Based Asymmetric System Mixer Model; Department of Electrical Engineering; University of South Florida. 2003	
MCJ	5	LIU ET AL.; Automated Measurement of Intermodulation Tables for Mixers; Center for Wireless and Microwave Information Systems; University of South Florida. 2002	

Examiner Signature	Mary C. Forcel	Date Considered	10/17/06
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